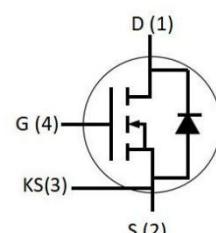


SIC MOSFET

Features

- High Blocking Voltage with Low On-Resistance
- Easy to Parallel and Simple to Drive
- High Speed Switching with Low Capacitances
- Avalanche Ruggedness
- Halogen Free, RoHS Compliant

BV _{DSS}	R _{DS(ON),typ.}	I _D
1200V	16mΩ	120A



TO-247-4

Package Not to Scale

Absolute Maximum Ratings

Symbol	Parameter	Maximum Rating	Unit
V _{DSS}	Drain-to-Source Voltage	1200	V
V _{GSS}	Gate-to-Source Operation Voltage	-5/+20	
I _D	Continuous Drain Current	120	A
	Continuous Drain Current @ T _c =100°C	80	
I _D pulse	Pulsed drain current (T _C =25°C, t _p limited by T _{jmax})	250	
P _D	Power Dissipation	430	W
T _J & T _{STG}	Operating and Storage Temperature Range	-55 to 175	°C

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

Thermal Characteristics

Symbol	Parameter	Maximum Rating	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	0.35	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	35	

Electrical Characteristics

OFF Characteristics $T_J = 25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	1200	--	--	V	$V_{GS}=0V, I_D=250\mu\text{A}$
I_{DSS}	Drain-to-Source Leakage Current	--	--	100	μA	$V_{DS}=1200\text{V}, V_{GS}=0\text{V}, T_c=25^\circ\text{C}$
I_{GSS}	Gate-to-Source Leakage Current	--	10	200	nA	$V_{GS}=18\text{V}, V_{DS}=0\text{V}$
I_{SGS}	Gate-to-Source Leakage Current	--	-10	-200	nA	$V_{GS}=-8\text{V}, V_{DS}=0\text{V}$

ON Characteristics $T_J = 25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
$R_{DS(ON)}$	Static Drain-to-Source On-Resistance	--	16	20	$\text{m}\Omega$	$V_{GS}=18\text{V}, I_D=60\text{A}, T_J=25^\circ\text{C}$
		--	30	--	$\text{m}\Omega$	$V_{GS}=18\text{V}, I_D=60\text{A}, T_J=175^\circ\text{C}$
$V_{GS(TH)}$	Gate Threshold Voltage	2.0	2.8	4.0	V	$V_{DS}=V_{GS}, I_D=25\text{mA}, T_J=25^\circ\text{C}$
		--	2.0	--	V	$V_{DS}=V_{GS}, I_D=25\text{mA}, T_J=175^\circ\text{C}$
g_{fs}	Transconductance	--	50	--	S	$V_{DS}=20\text{V}, I_D=60\text{A}$
R_G	Gate resistance	--	2.4	--	Ω	$V_{GS}=0\text{V}, V_{AC}=25\text{mV}, f=1.0\text{MHz}$

Dynamic Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
C_{iss}	Input Capacitance	--	6145	--	pF	$V_{GS}=0\text{V}, V_{DS}=1000\text{V}, f=1.0\text{MHz}, V_{AC}=25\text{mV}$
C_{rss}	Reverse Transfer Capacitance	--	11	--		
C_{oss}	Output Capacitance	--	230	--		
Q_g	Total Gate Charge	--	200	--	nC	$V_{DD}=800\text{V}, I_D=60\text{A}, V_{GS}=-5 \text{ to } +18\text{V}$
Q_{gs}	Gate-to-Source Charge	--	64	--		
Q_{gd}	Gate-to-Drain (Miller) Charge	--	80	--		

Resistive Switching Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
$t_{d(ON)}$	Turn-on Delay Time	--	27	--	ns	$T_J=25^\circ C$, $V_{GS}=-5/+18V$, $ID=50A$, $V_{DS}=800V$, $R_g=5\Omega$, $L=200\mu H$
t_r	Rise Time	--	18	--		
$t_{d(OFF)}$	Turn-Off Delay Time	--	75	--		
t_f	Fall Time	--	46	--		

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
E_{ON}	Turn-On Switching Energy	--	0.46	--	mJ	$T_J=25^\circ C$, $V_{GS}=-5/+18V$, $ID=50A$, $V_{DS}=800V$, $R_g=5\Omega$, $L=200\mu H$
E_{OFF}	Turn-Off Switching Energy	--	1.0	--		

Source-Drain Body Diode Characteristics

Symbol	Parameter	Min	Typ.	Max.	Unit	Test Conditions
V_{SD}	Diode Forward Voltage	--	3.2	--	V	$V_{GS}=-5V$, $I_{SD}=30A$, $T_j = 25^\circ C$
		--	2.3	--		$V_{GS}=-5V$, $I_{SD}=30A$, $T_j = 175^\circ C$
t_{rr}	Reverse recovery time	--	18.6	--	ns	$V_{GS}=-5/+18V$, $I_{sd}=50A$, $V_R=800V$, $T_j = 25^\circ C$
Q_{rr}	Reverse recovery charge	--	130	--		
I_{rrm}	Peak Reverse Recovery Current	--	10.6	--		



Typical Performance Characteristics

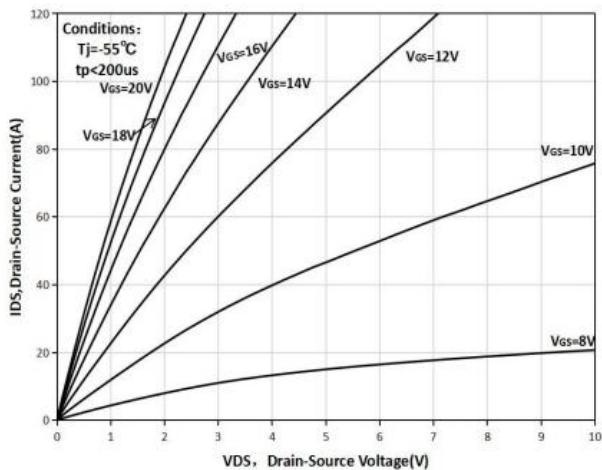


Figure 1. Output Characteristics $T_J = -55^\circ\text{C}$

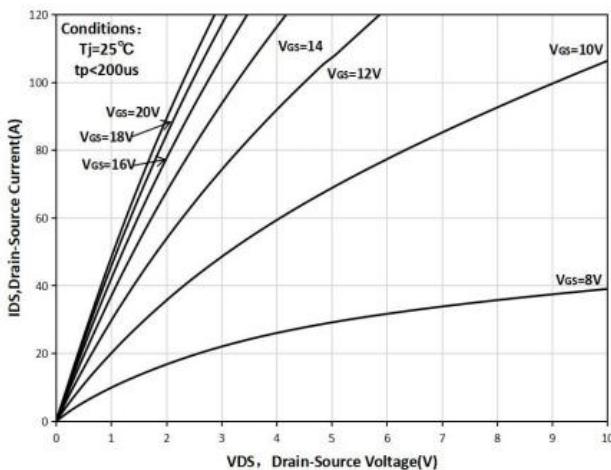


Figure 2. Output Characteristics $T_J = 25^\circ\text{C}$

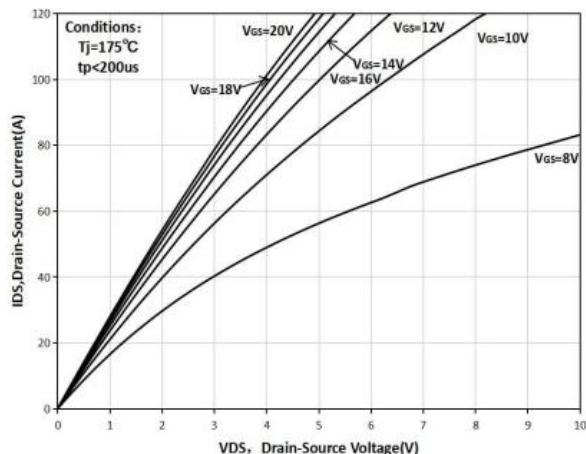


Figure 3. Output Characteristics $T_J = 175^\circ\text{C}$

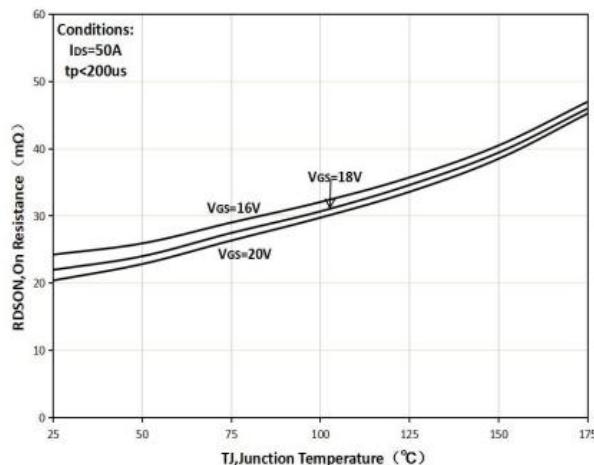


Figure 4. On-Resistance For Various Gate Voltage

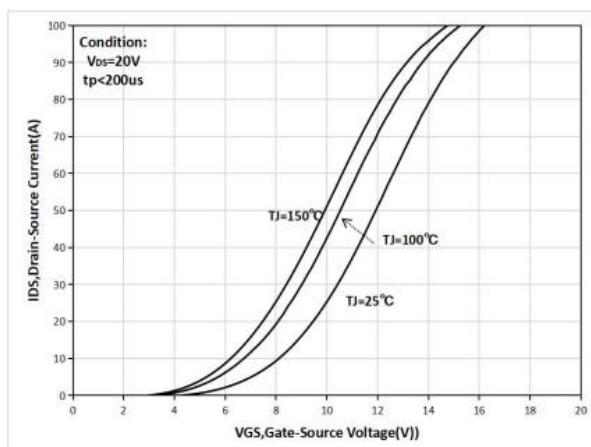


Figure 5. Transfer Characteristic for Various Junction Temperatures

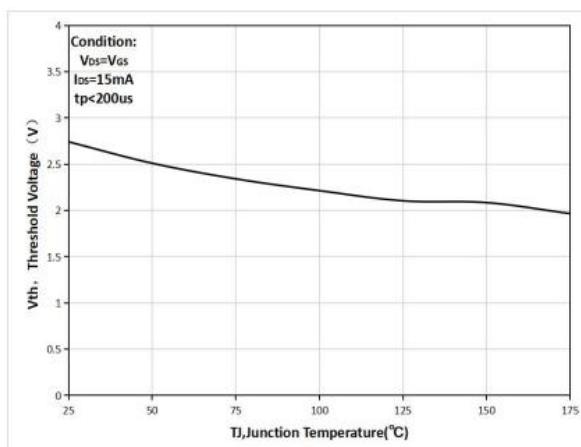


Figure 6. Threshold Voltage vs. Temperature

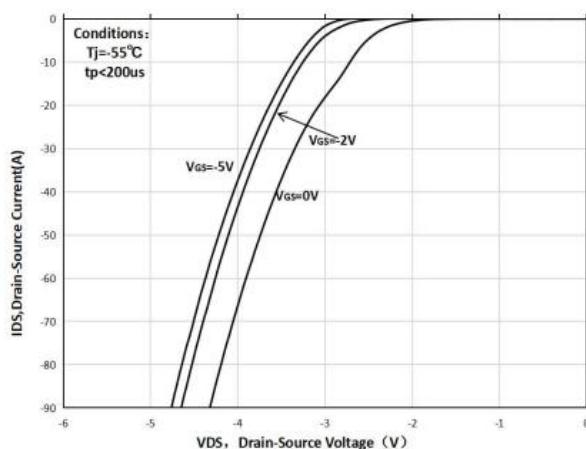


Figure 7. Body Diode Characteristics at -55°C

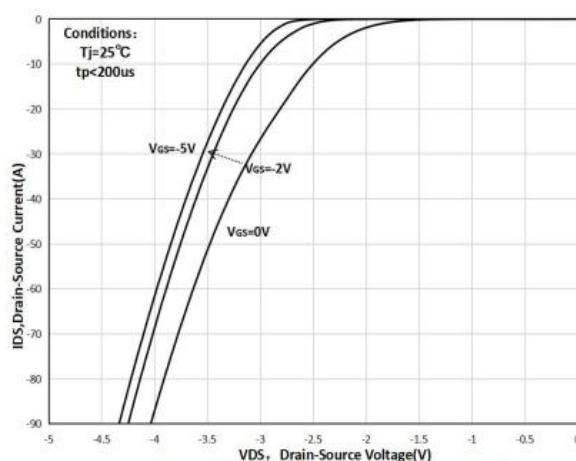


Figure 8. Body Diode Characteristics at 25°C

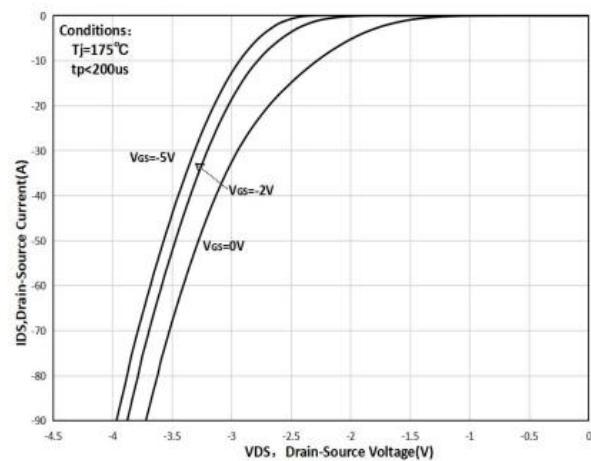


Figure 9. Body Diode Characteristics at 175°C

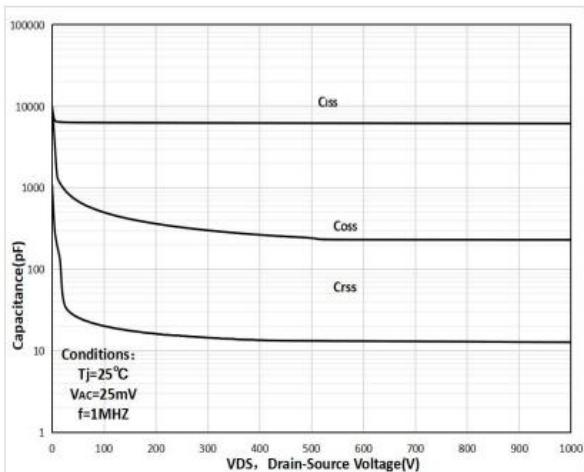


Figure 10. Capacitances vs. Drain-Source Voltage

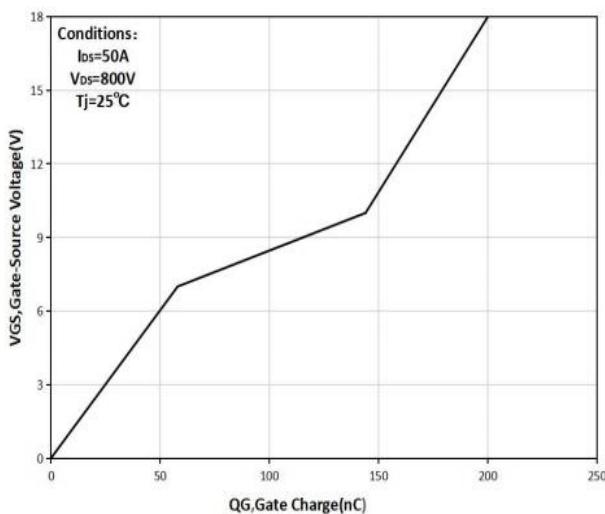


Figure 11. Gate Charge Characteristics

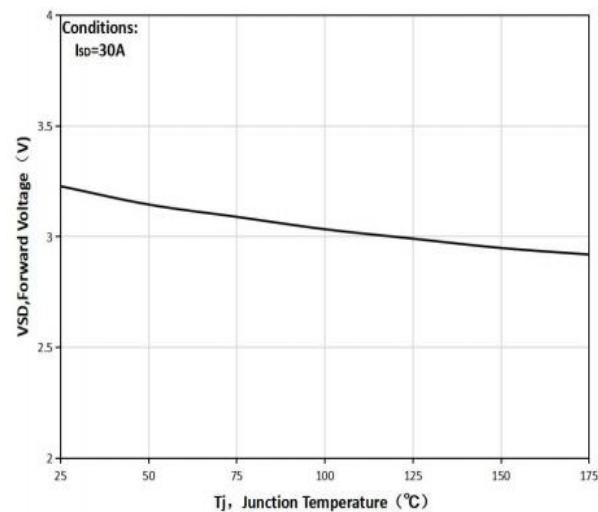


Figure 12. Forward Voltage vs. Junction Temperature

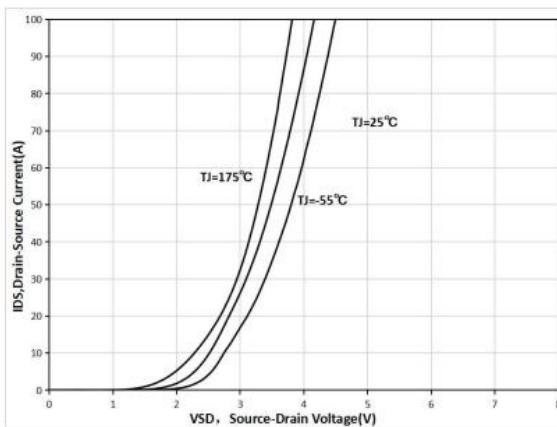


Figure 13. Body Diode Characteristics for Various Junction Temperatures

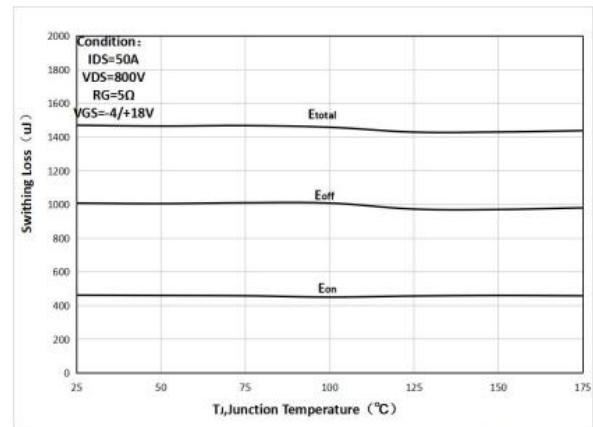


Figure 14. Clamped Inductive Switching Energy Vs. Temperature

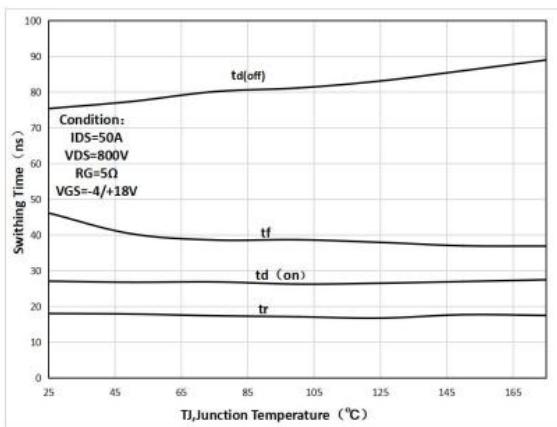


Figure 15. Switching Times vs. Junction Temperature

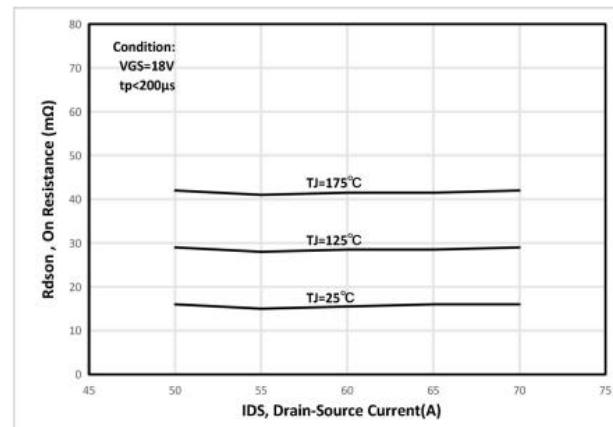


Figure 16. On-Resistance vs. Drain Current

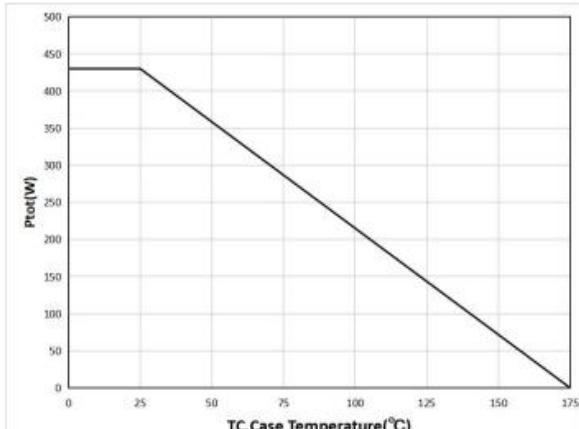


Figure 17. Power Dissipation Derating

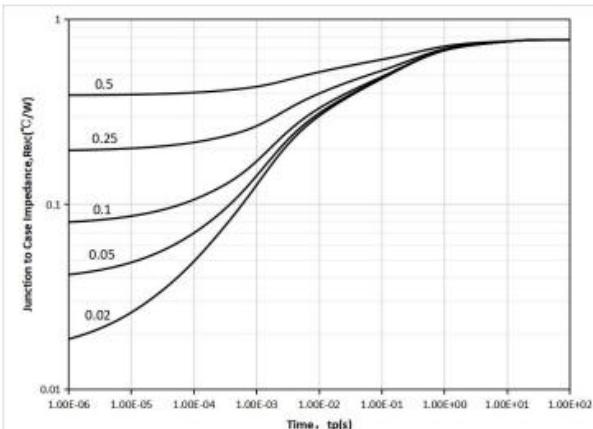


Figure 18. Transient Thermal Impedance

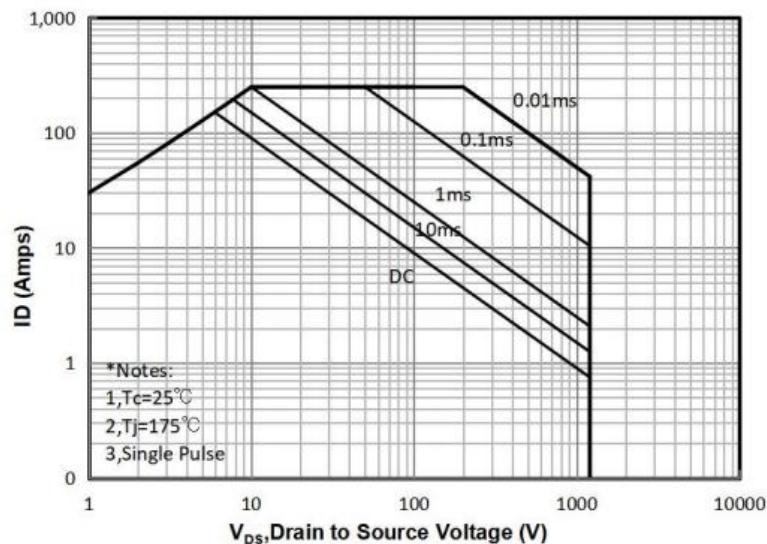
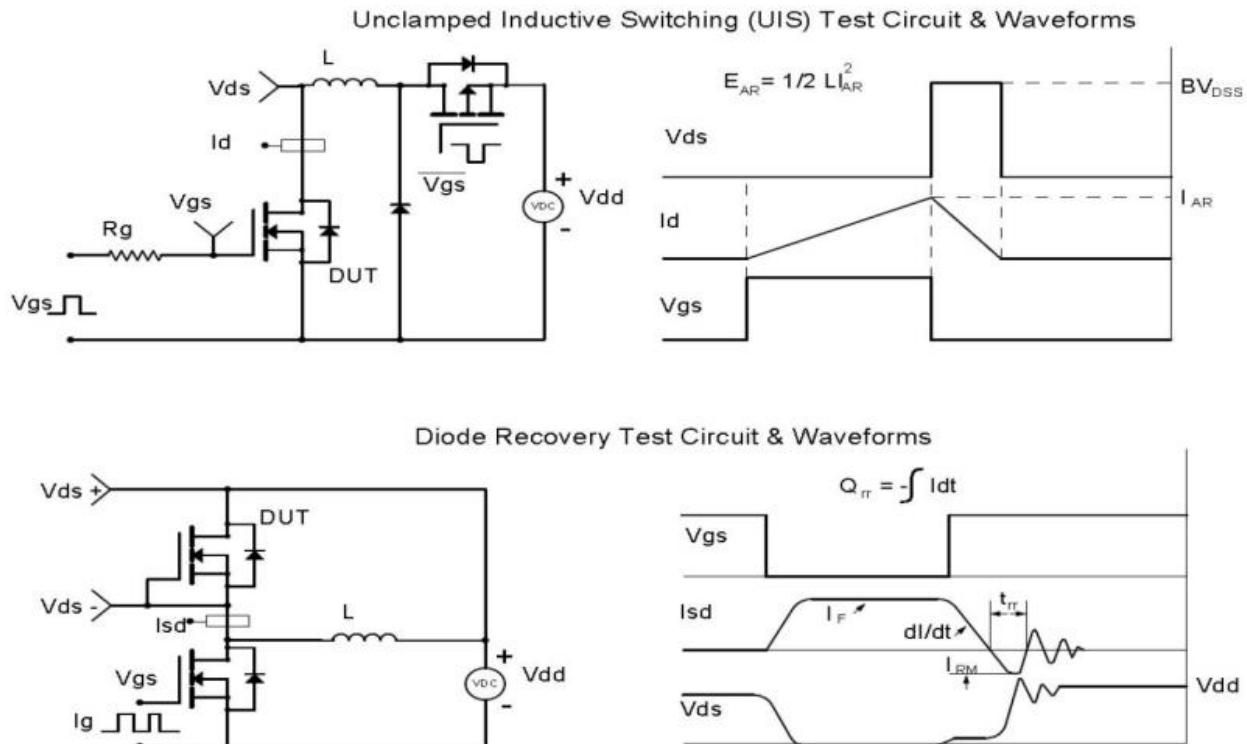
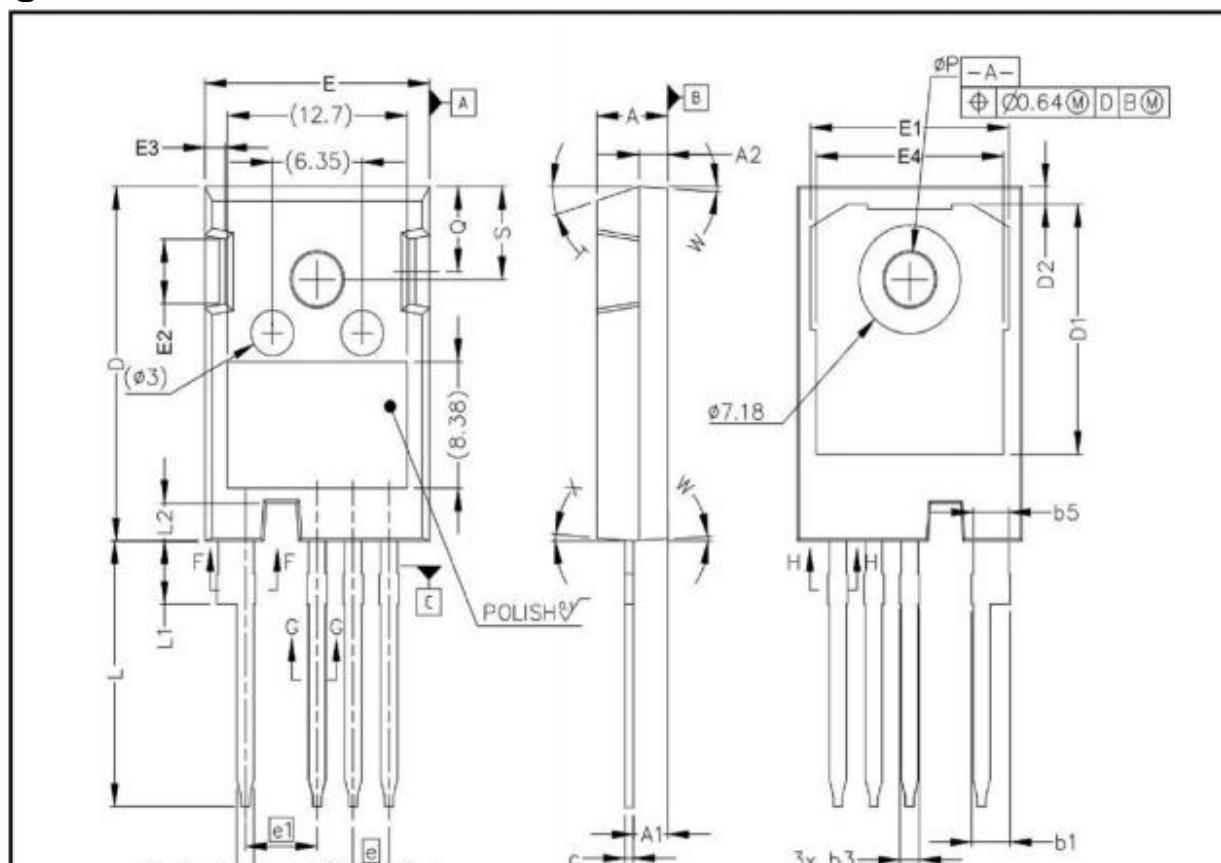


Figure 19. Safe Operating Area

Test Circuit Waveforms



Package Dimensions: TO-247-4L



SYMBOL	Mechanical Dimensions/mm			SYMBOL	Mechanical Dimensions/mm			SYMBOL	Mechanical Dimensions/mm		
	MIN	NOM	MAX						MIN	NOM	MAX
A	4.83	5.00	5.21	D	23.30	23.45	23.60	L1	3.97	4.13	4.37
A1	2.29	2.41	2.54	D1	16.25	16.55	17.65	Ø P	3.51	3.6	3.65
A2	1.91	2.00	2.16	E	15.75	15.90	16.13	W	-	3.5	-
b	1.07	1.20	1.33	E1	13.10	13.65	14.15	X	-	4	-
b1	2.39	2.60	2.94	E2	3.68	5.0	5.1	Q	5.49	5.8	6.0
b2	2.39	-	2.84	e	2.54			S	6.04	6.15	6.30
c	0.55	0.60	0.68	L	17.31	17.45	17.62	T	-	17.5	-

NOTE:

1.The plastic package is not marked as smooth surfaceRa=0.1;Subglossy surfaceRa=0.8

2.Undeclared tolerance±0.15,Unmarked filletRmax=0.25

NAME	TO-247-4L OUTLINE	UNIT	mm	DESIGNED	Shawn	THIRD ANGLE SYSTEM
DWGNO		PAGE	1 OF 1	CHECKED		
VERSION	Ver1.0	ISSUE DATE		APPROVED		

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